



Shri S'ad Vidya Mandal Institute of Technology
College Campus, Old National Highway No.: 08
Bharuch – 392 001, Gujarat , India

Phone : (02642) 245864 Fax : (02642)249601

09-01-2018

**Time table of 3rd SEM BACKLOG Examination
March April -2018
(Electrical Engineering)**

Sr. No	Subject	Code	Date	Time
1	Advanced Engineering Mathematics	2130002	27-03-2018	10:30 TO 11:30 am
2	Engineering Economics and Management	2130004	27-03-2018	1:30 TO 2:30 pm
3	Analog Electronics	2130902	03-04-2018	10:30 TO 11:30 am
4	DC Machines and Transformer	2130904	03-04-2018	1:30 TO 2:30 pm
5	Circuits and Networks	2130901	05-04-2018	10:30 TO 11:30 AM
6	Electrical Measurement and Measuring Instruments	2130903	05-04-2018	1:30 TO 2:30 pm

(Haresh S. Nankani)

(Prof. PAYAL P. PATEL)

EXAM CO-ORDINATOR

HOD, ELECTRICAL ENGG. DEPT.



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Date:09-01-2018

**Time table of 5th SEM BACKLOG Examination
MARCH APRIL -2018
(Electrical Engineering)**

Sr. No	Subject	Code	Date	Time
1	Electrical Power System – I	2150908	27-03-2018	12:00 noon To 1:00 pm
2	Control System Engineering	2150909	27-03-2018	2:45 TO 3:45 p.m.
3	Elements of Electrical Design	2150904	03-04-2018	12:00 noon To 1:00 pm
4	Microprocessor and Microcontroller Architecture & Interfacing	2150907	03-04-2018	2:45 TO 3:45 p.m.
5	Power Electronics – I	2150903	05-04-2018	12:00 noon To 1:00 pm

(Haresh S. Nankani)
EXAM CO-ORDINATOR

(Prof. PAYAL P. PATEL)
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09-01-2018

**Time table of 7th SEM BACKLOG Examination
MARCH APRIL-2018
(Electrical Engineering)**

Sr. No	Subject	Code	Date	Time
1	Inter Connected Power System	2170901	27-03-2018	12:00 noon To 1:00 pm
2	Switchgear & Protection	2170908	27-03-2018	2:45 TO 3:45 p.m.
3	Design of AC Machines	2170909	03-04-2018	12:00 noon To 1:00 pm
4	Industrial Instrumentation(Elective)	2170913	03-04-2018	2:45 TO 3:45 p.m.

(Haresh S. Nankani)

EXAM CO-ORDINATOR

(Prof. PAYAL P. PATEL)

H.O.D.

ELECTRICAL ENGG. DEPT.

