

Shri S'ad Vidya Mandal Institute of Technology  
 Department of CSE & IT  
 Timetable - Backlog Examination - MARCH/APRIL 2018

BE - Final Year - Sem VII - CE & IT

Sr. No.	Date	Time	Subject	
			CE	IT
1	27/03/2018	12:00 TO 1:00 PM	INS	INS
2		2:45 PM TO 3:45 PM	DMBI	DMBI
3	03/04/2018	12:00 TO 1:00 PM	MCWC	MCWC
4		2:45 PM TO 3:45 PM	CD	DDBMS

BE - Third Year - Sem V - CE & IT

Sr. No.	Date	Time	Subject	
			CE	IT
1	27/03/2018	12:00 TO 1:00 PM	MI	CG
2		2:45 PM TO 3:45 PM	OOPJ	OOPJ
3	03/04/2018	12:00 TO 1:00 PM	ADA	ADA
4		2:45 PM TO 3:45 PM	SP	SP

BE - Second Year - Sem III - CE & IT

Sr. No.	Date	Time	Subject	
			CE	IT
1	27/03/2018	10:30 AM TO 11:30 AM	AEM	AEM
2		1:30 PM TO 2:30 PM	EEM	EEM
3	03/04/2018	10:30 AM TO 11:30 AM	DS	DS
4		1:30 PM TO 2:30 PM	DBMS	DBMS
5	19/04/2018	10:30 AM TO 11:30 AM	DE	DE

Note: Students with internal backlog in "Cyber Security" should contact subject teacher

Prof. V. R. Patel  
 HOD, CSE & IT