

		<b>SHRI S'AD VIDYA MANDAL INSTITUTE OF TECHNOLOGY, BHARUCH</b> <b>TIMETABLE ACADEMIC YEAR 2017-18 DEPT: CSE &amp; IT</b> <b>B.E-I (1st Semester) CSE</b>				<b>ROOM NO : 02 (GF)</b> <b>ELECTRICAL BUILDING</b>	
		<b>TIME/DAY</b>	<b>MONDAY</b>	<b>TUESDAY</b>	<b>WEDNESDAY</b>	<b>THURSDAY</b>	<b>FRIDAY</b>

8:30 am to 9:30 am	CPU (MVC)	CAL (MX1)	EEM (PPP)	CPU (VMP)	EEE (PPP)
9:30 am to 10:30 am	EME (MMMM)	EME (HP)	CAL (MX1)	EEE (PPP)	CPU (VMIP)

<b>SHORT BREAK</b>						
10:30 am to 10:45 am	CS (X1)	EEE (A) (MR)	CPU (B) MVC	W/S ELECT (B) (X1)	EME (MMMM)	CS (B) (X1)
10:45 am to 11:45 am	EEE (PPP)				CAL (MX1)	W/S ELECT (A) (X1)

<b>RECESS</b>						
12:45 pm to 01:15 pm	CPU (MVC)	W/S EC (A) (X1)	EME (HP)	CS (X1)	W/S EC (B) (X2)	
01:15 pm to 02:15 pm	EME (A) (HGS)	EEE (B) (MR)		CPU (A) (MVC)	CAL (B) (MX1)	EME (B) (HP)
02:15 pm to 03:15 pm					CAL (A) (MX1)	
03:15 pm to 04:00 pm						

SUBJECTS	
CPU	Computer Programming & Utilization
EME	Elements of Mechanical Engg.
CS	Communication Skill
EEE	Elements of Electrical Engg.
CAL	CALCULUS
EC WS	
ELECT WS	

FACULTY DETAILS	
VMP	Prof. Vishal M. Patel
RRP	Prof. Ripal R. Patel
MMMM	Prof. Mayur Makwana
PPP	Prof. Payal Patel
MR	Prof. Manish Rana
HGS	Prof. H G shah
HP	Prof. Hussain Pipwala

Shri J. M. PATEL	DR. R. G. KAPADIA	PROF. V. R. PATEL	PROF. K. J. DANGARWALA	PROF. N.H.MISTRY, PROF. A.D.PATEL
DIRECTOR	PRINCIPAL	HOD OF CSE & IT	TIME TABLE CO-ORDINATOR	DEPT TIME TABLE CO-ORDINATOR